

Electronic Acknowledgement Receipt

EFS ID:	2132261
Application Number:	10608426
International Application Number:	
Confirmation Number:	8083
Title of Invention:	METHOD FOR FORMING A SEMICONDUCTOR DEVICE WITH A HARD MASK LAYER FORMED OVER A BIT LINE TO PROTECT THE BIT LINE DURING SUBSEQUENT ETCHING STEPS
First Named Inventor/Applicant Name:	Jung Taik Cheong
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Attorney Docket Number:	40296-0029
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Application Type:	Utility under 35 USC 111(a)

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Submitted with Payment	no
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Power of Attorney	GeneralPOA1forHEcases.pdf	196242 c4b8428ade20fa2f75e3354cdd0eb721521851b	no	2

Warnings:

Information:					
2	Assignee showing of ownership per 37 CFR 3.73(b).	37CFR373b.pdf	70158	no	1
			f3bf9b18affbfb1ef13915aeb663f1148b96872c6		
Warnings:					
Information:					
Total Files Size (in bytes):				266400	
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